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NanoDx™ Announces Licensing Collaboration with IBM Research for the Design and Manufacturing of Nanoscale Sensors for Rapid Diagnostic Testing
Retraction notice to "Graphene quantum dots decorated CdS doped graphene oxide sheets in dual action mode: as initiator and platform for designing of nimesulide

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imprinted polymer " [BIOS 89P1 ...

Biosensors & bioelectronics

This project is developing a mixed-mode Fully-Depleted Complementary Metal Oxide Semiconductor (FD CMOS) technology suitable for scientific applications. This technology will offer higher speed ...

ABSTRACTS - Phase I

It is a generic technique and any protein of interest can be detected with this immunoassay platform. A similar story as for photonics-based biosensors can be told ... light source (e.g. LED or laser) ...

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How Chip Technology Can Help Diagnostics
Manufacturers

The smart contact lens is embedded with a biosensor, an f-DDS, a wireless power transmission system from a transmitter coil to a receiver coil, an ASIC chip, and a remote communication system as a ...

Wireless smart contact lens for diabetic diagnosis and
therapy

manufacturing will use CMOS production, with the same
degree of flexibility in optimisation and modification as
for electronic ICs.

New records: format, price and precision

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These components will include a low-autofluorescence waveguide platform, splitters ... This work will be guided by select applications, including biosensors, optical coherence tomography (OCT) imagers ...

Biophotonics: PIX4life targets biophotonics with visible-range PICs, development capacity build
Novel electronic applications with 2D materials and nanowires for biosensors: For example ... This will be a good platform for developing low-cost diagnostic devices for global health problems (HIV, ...)

Jeongwon Park

His research established new transformative biosensor

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platform technology that is in commercial use by Resonant Sensors (Arlington, TX), a company he co-founded. Magnusson has garnered more than \$10 ...

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Dual work function metal gates are integrated at 17nm spacing between n- and pFETs, highlighting the key benefit of forksheet devices for advanced CMOS area scaling. The forksheet device has recently ...

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